

FIGURE 1

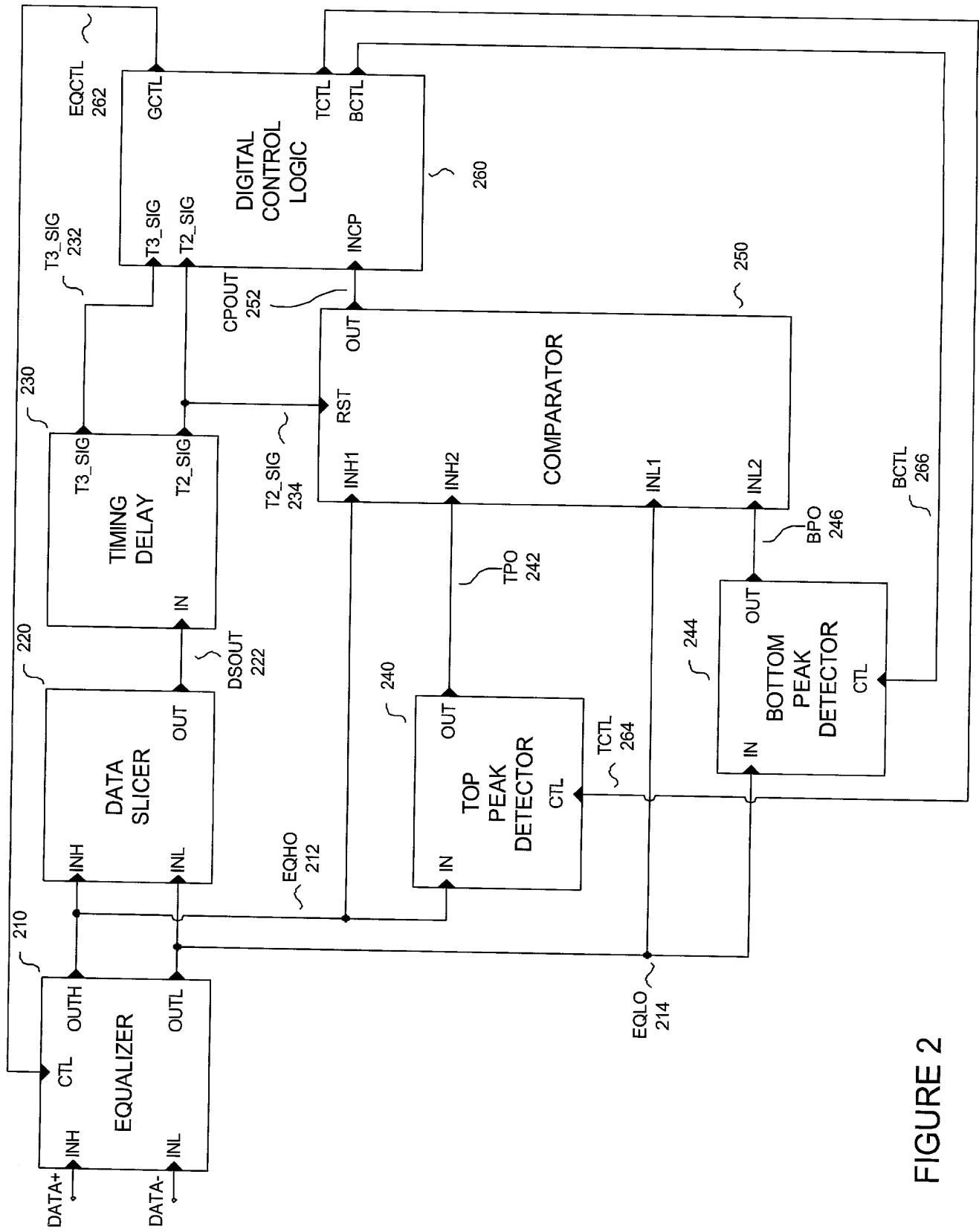


FIGURE 2

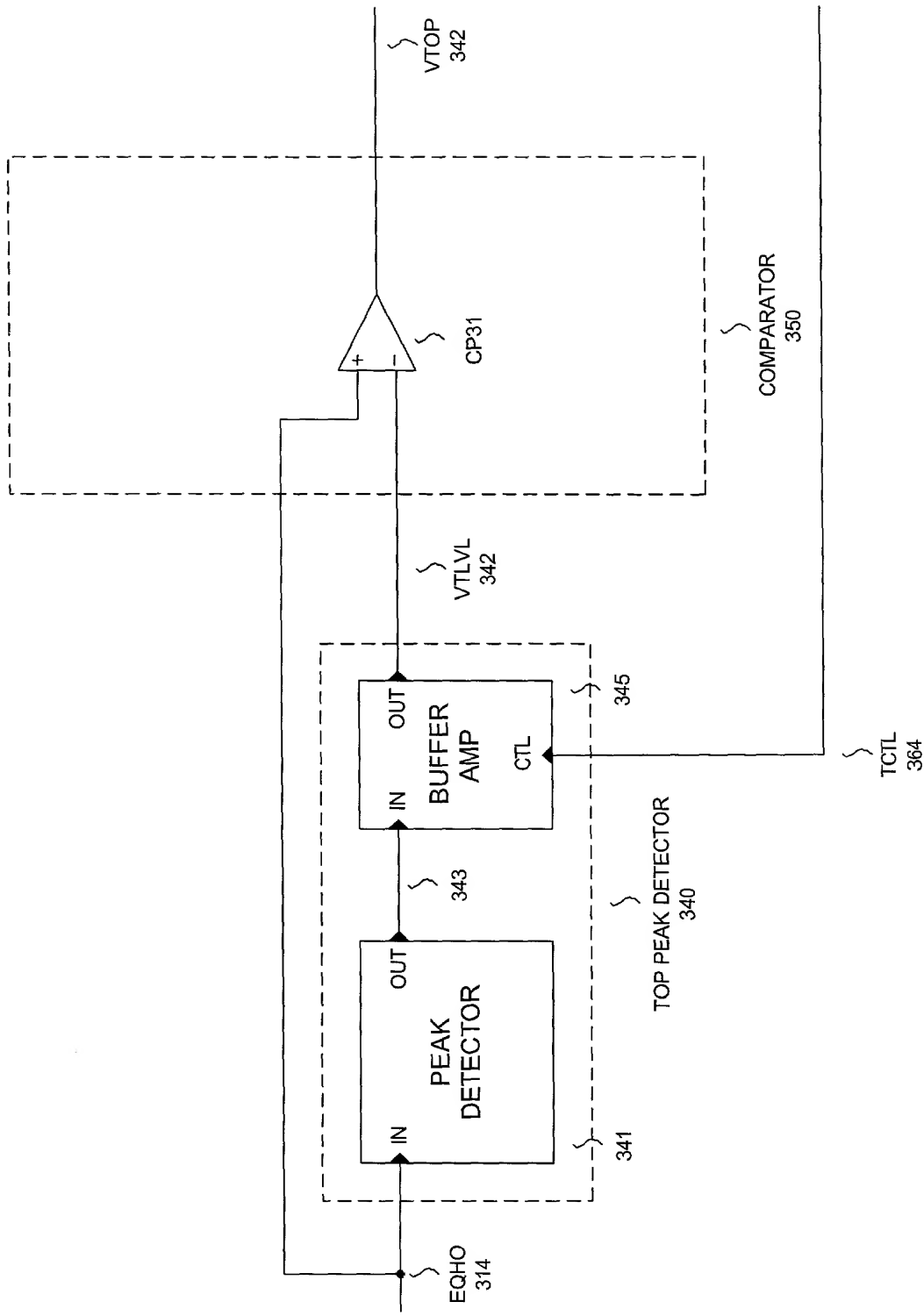


FIGURE 3

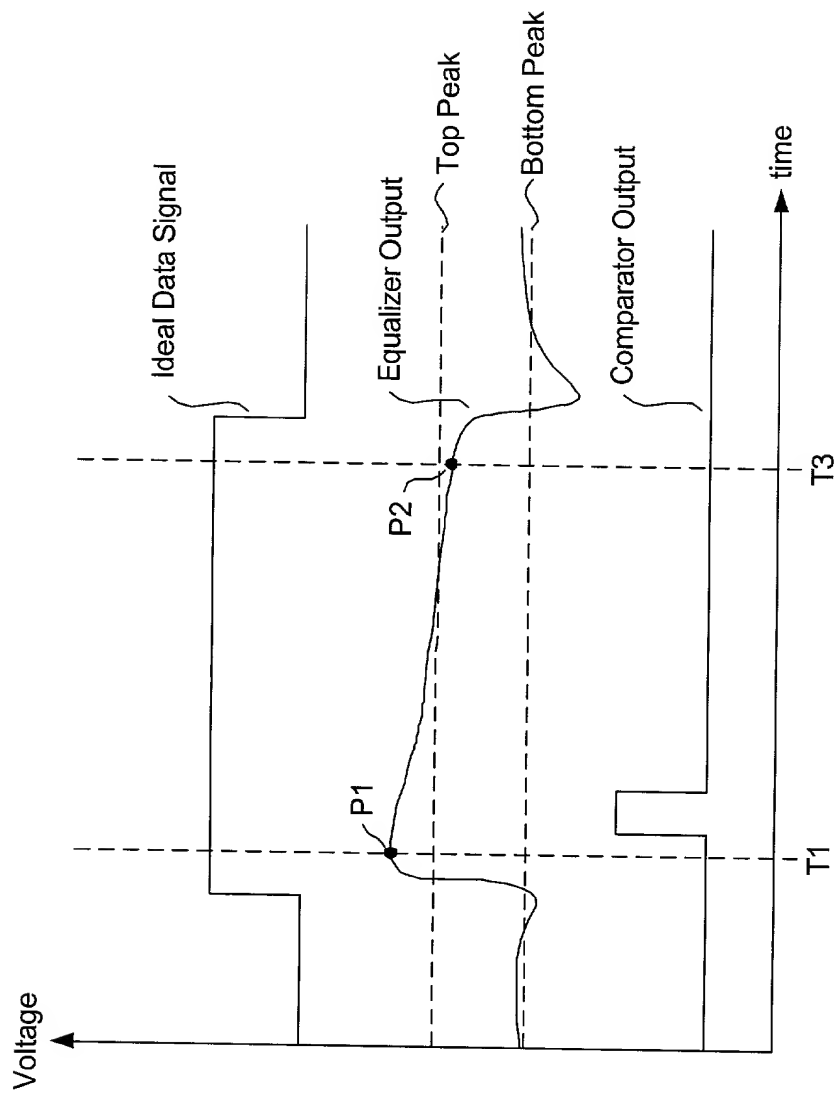


FIGURE 4

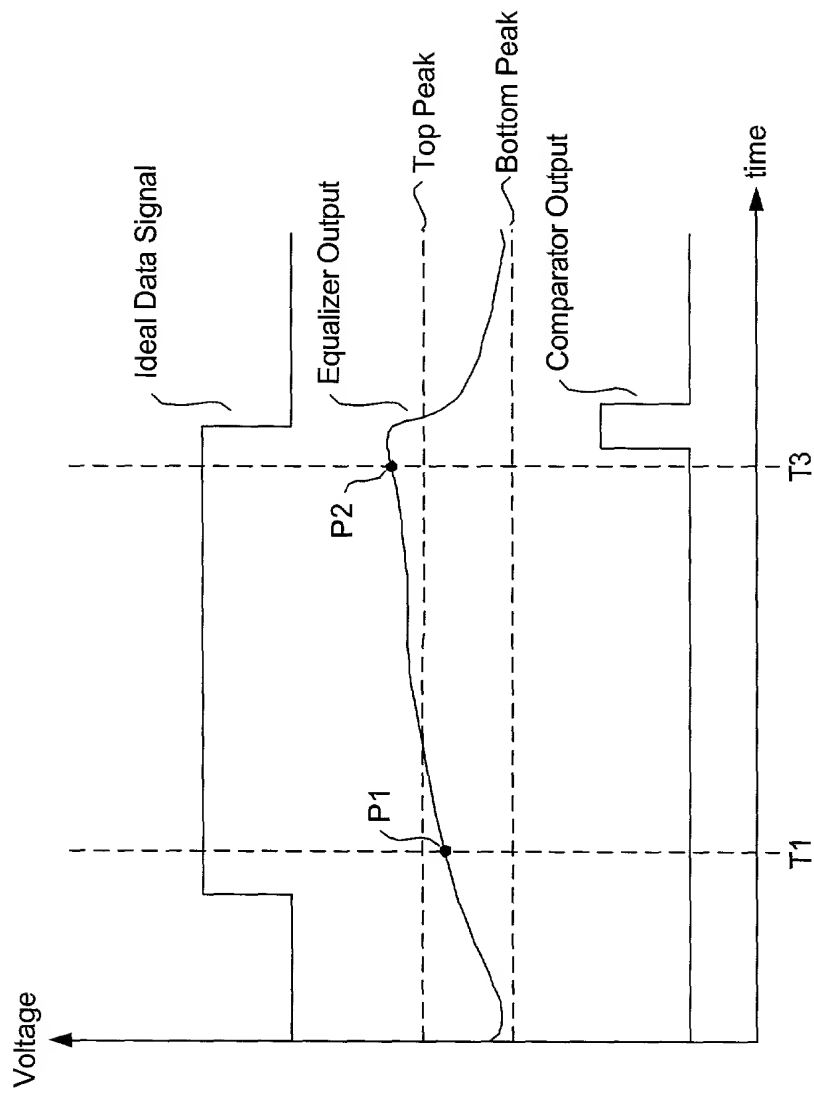


FIGURE 5

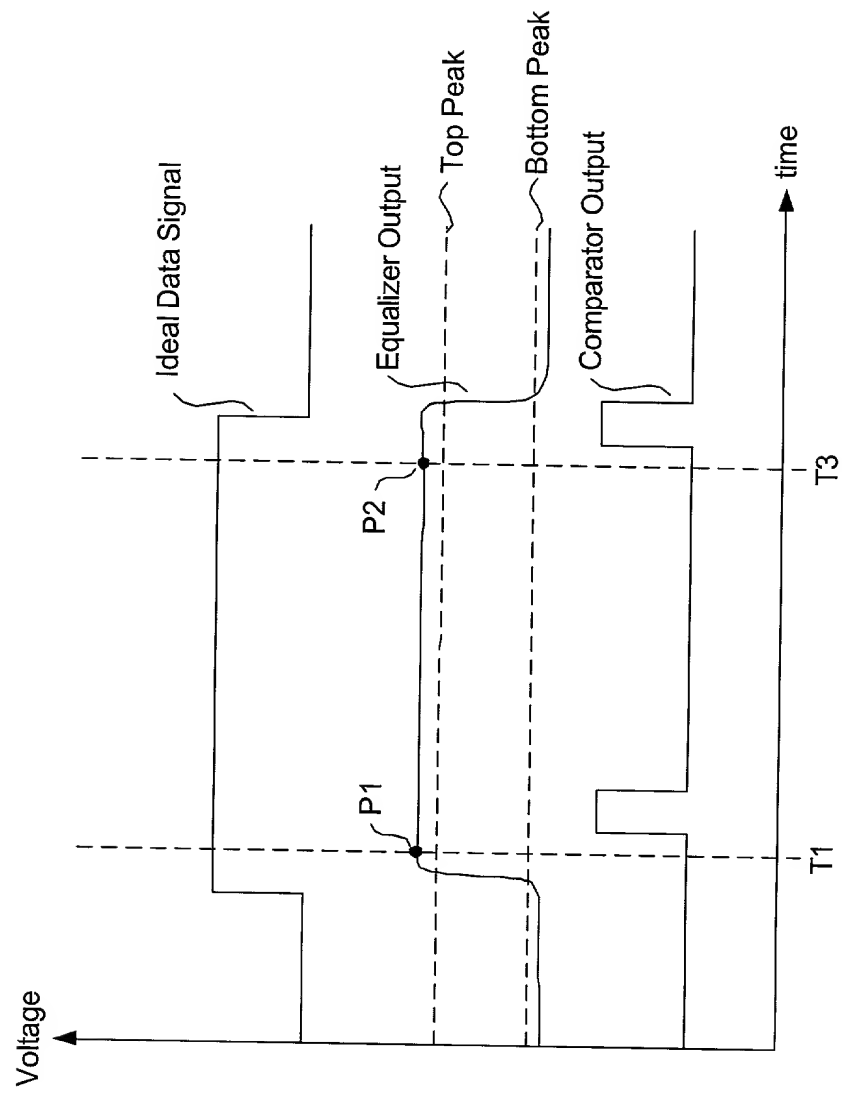


FIGURE 6

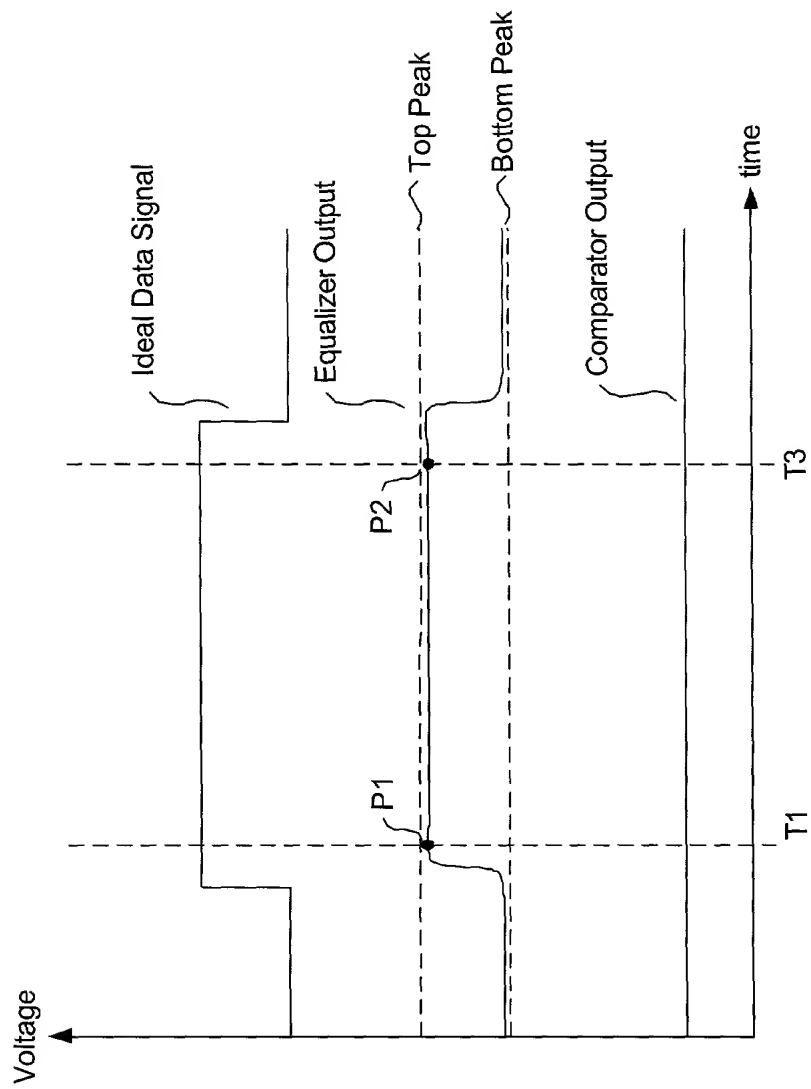


FIGURE 7

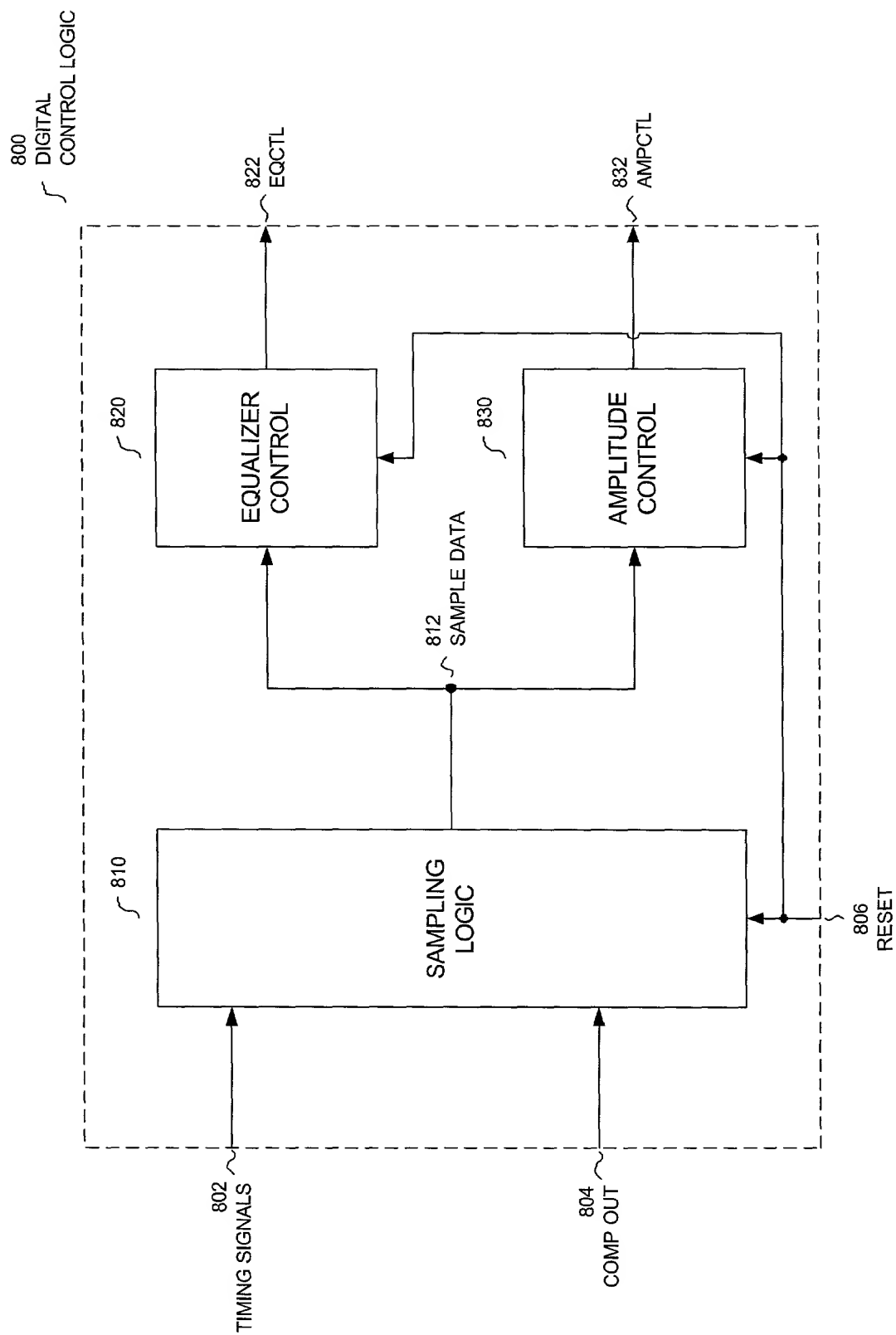


FIGURE 8



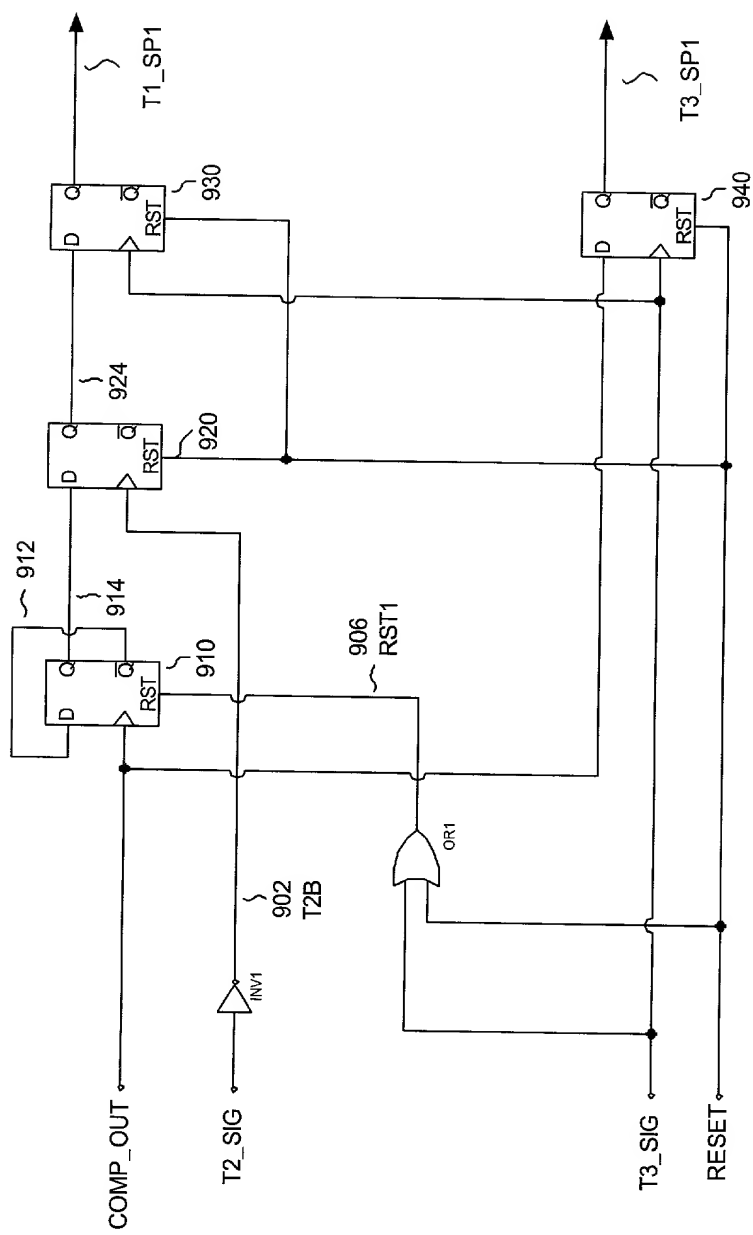


FIGURE 9

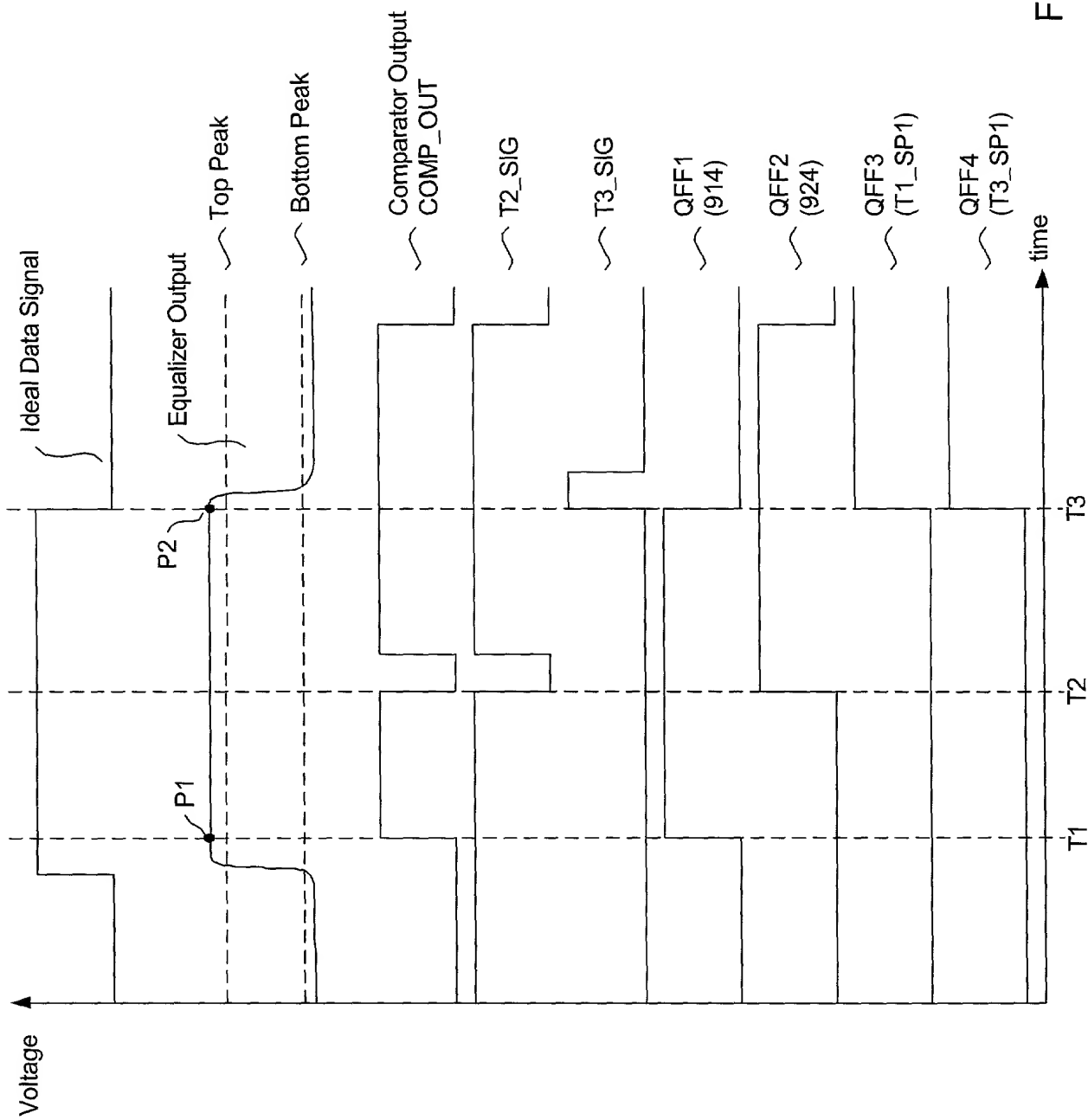


FIGURE 10

FIG. 11 is a timing diagram illustrating the operation of the system during a data burst. The diagram shows the relationship between the Ideal Data Signal, Equalizer Output, comparator output (COMP\_OUT), and the outputs of the comparators (QFF1, QFF2, QFF3, QFF4) over time. The time axis is marked with T1, T2, and T3. The voltage axis is labeled Voltage. The Ideal Data Signal is a square wave. The Equalizer Output is a curve that follows the Ideal Data Signal but with some distortion. The comparator output (COMP\_OUT) is a square wave that is high when the Ideal Data Signal is high and low when it is low. The outputs of the comparators (QFF1, QFF2, QFF3, QFF4) are square waves that are high when the Ideal Data Signal is high and low when it is low.

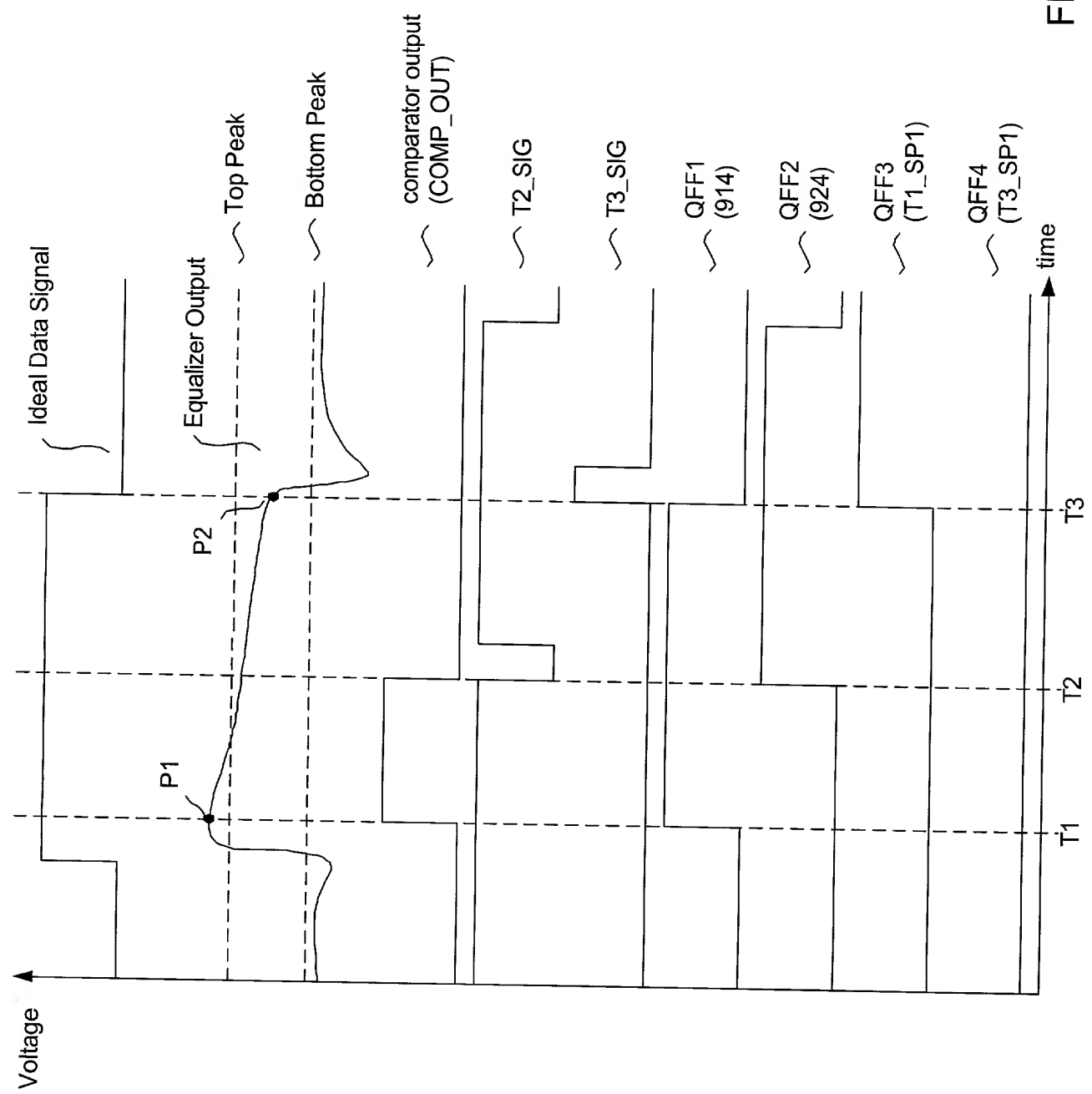


FIGURE 11

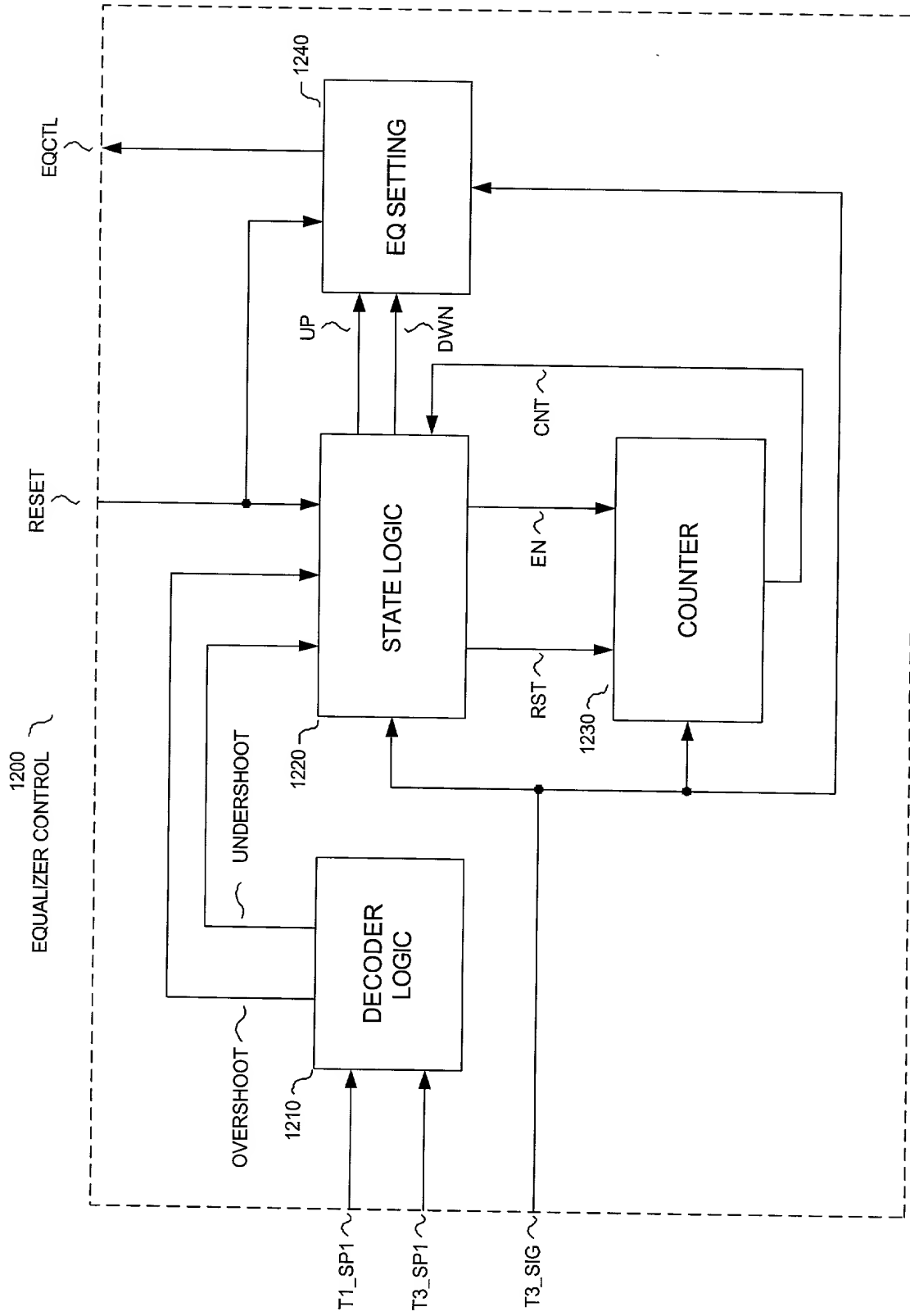


FIGURE 12

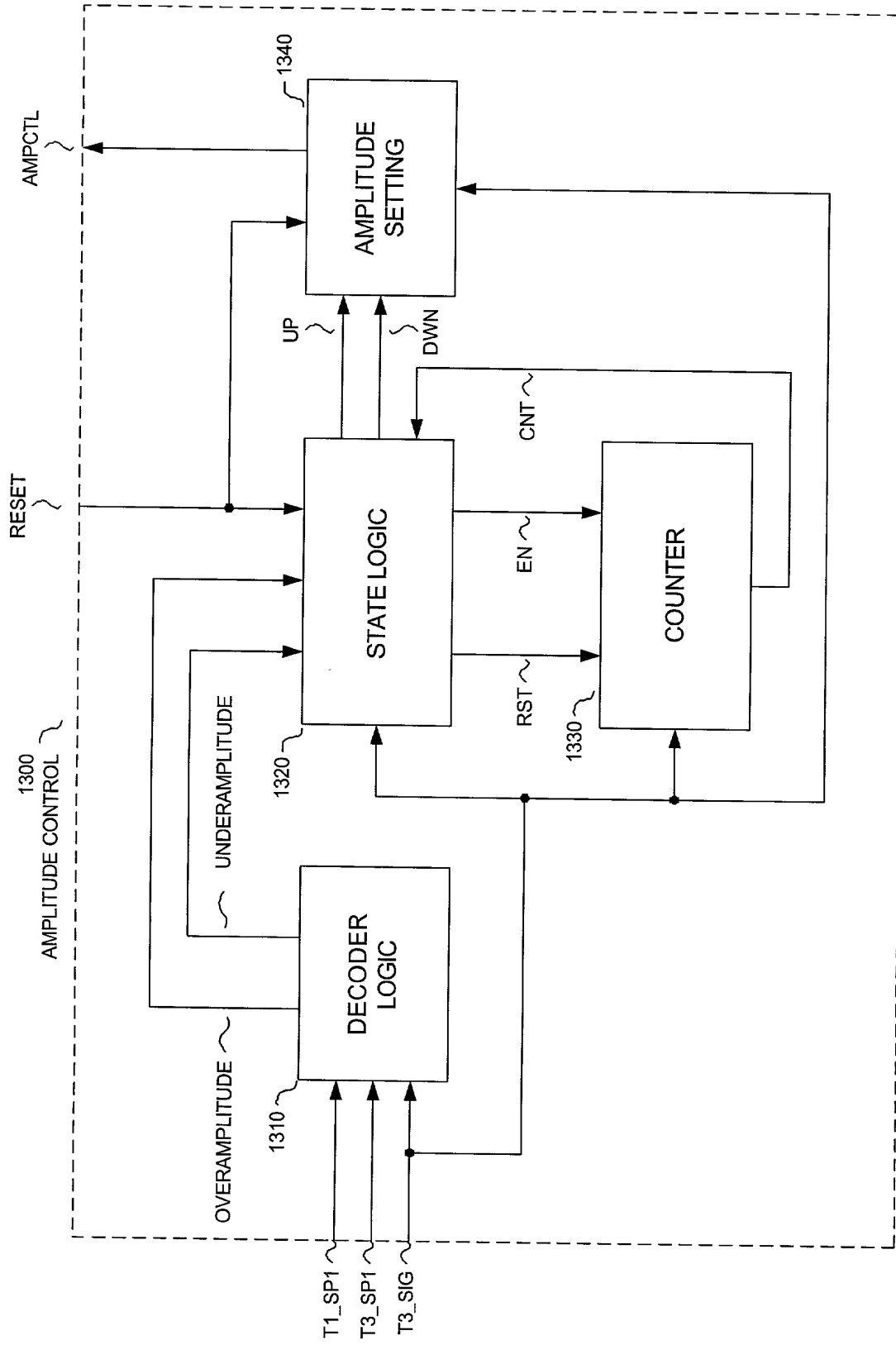


FIGURE 13